



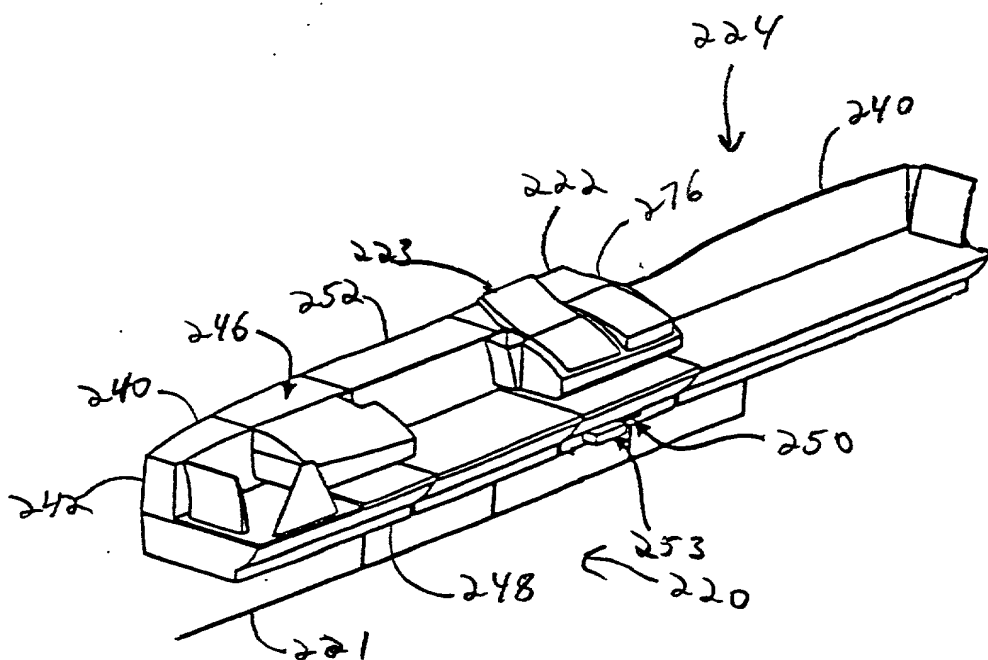
[illegible]

FIG. 3 is a block diagram of a system 300, including a processor 302, a memory 304, and a network interface 306. The processor 302 is connected to the memory 304 and the network interface 306. The memory 304 is connected to the network interface 306. The network interface 306 is connected to a network 308. The system 300 is configured to receive data from the network 308 and store the data in the memory 304. The processor 302 is configured to process the data stored in the memory 304. The network interface 306 is configured to transmit the processed data to the network 308.

FIG. 3

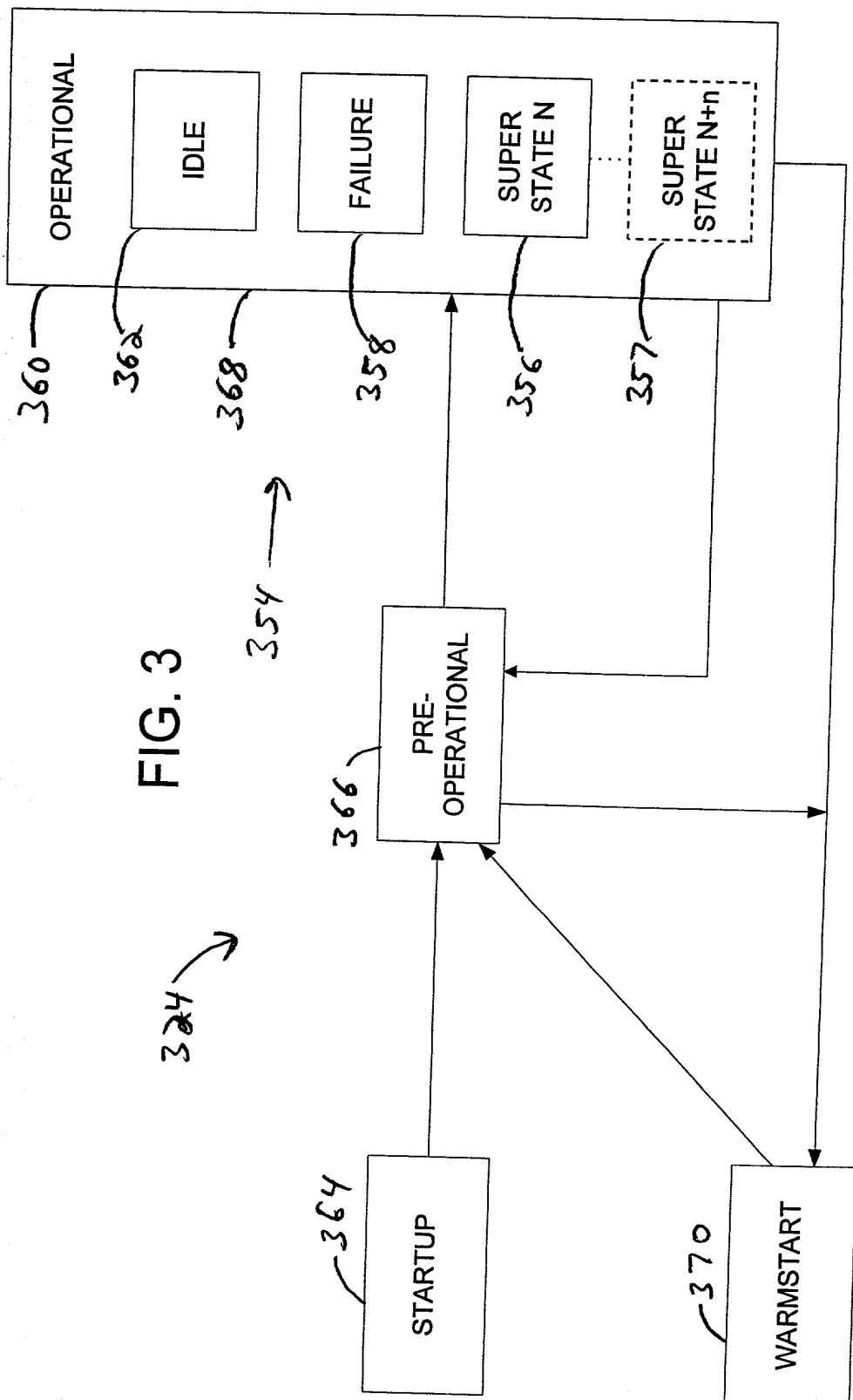


FIG. 4

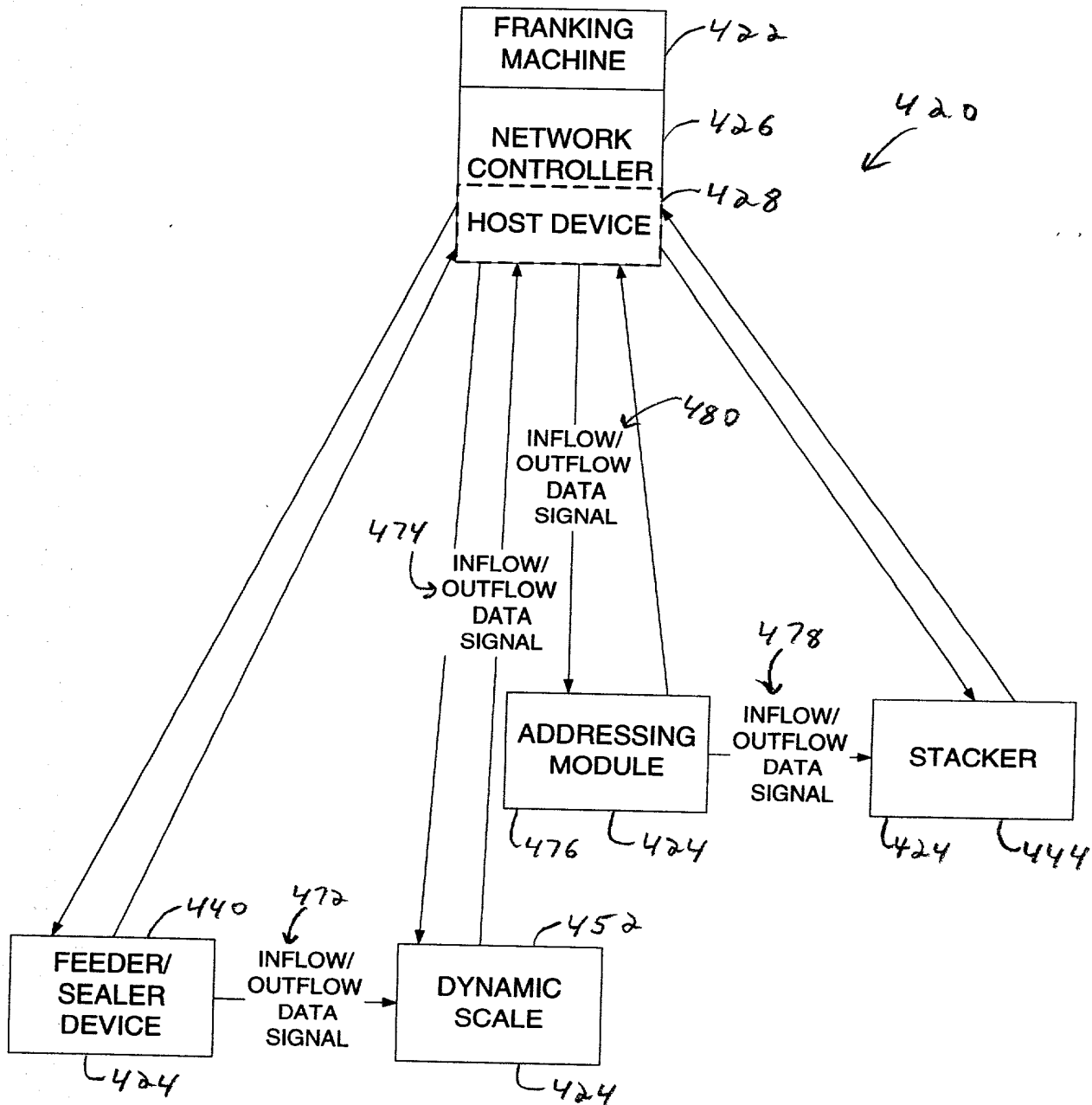


FIG. 5

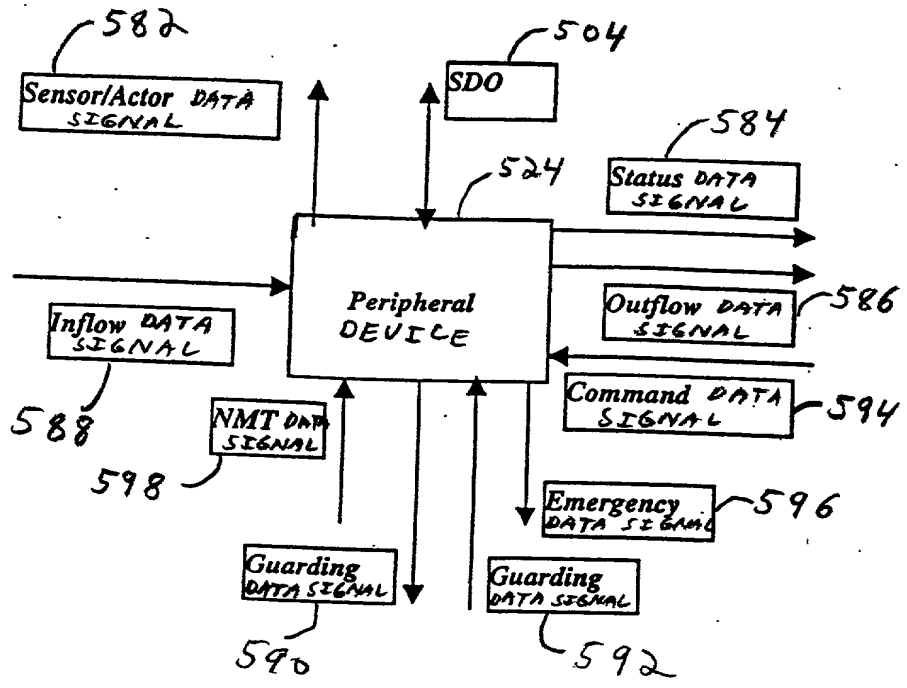


FIG. 6

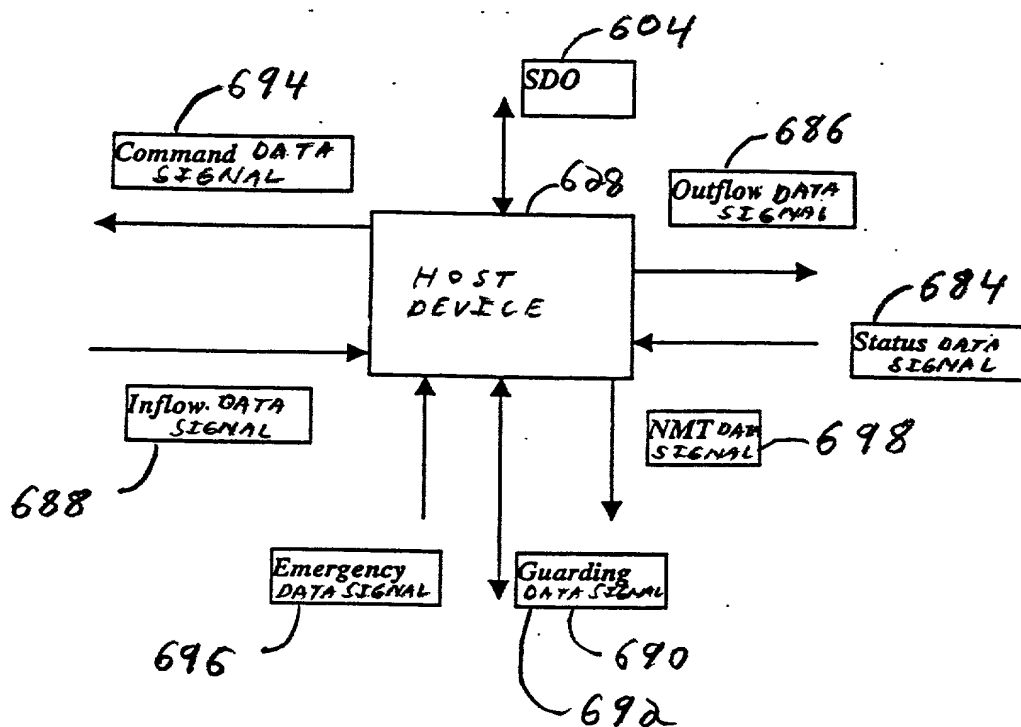


FIG. 7

